IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (Case No. 213.003-D2) In the Application of: YE ET AL.) Group Art Unit:) Serial No: DIVISIONAL OF 10/677,563) Examiner:) Filed: JANUARY 22, 2004) Title: METHOD AND APPARATUS FOR MONITORING INTEGRATED CIRCUIT FABRICATION)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Preliminary to the examination of the above-referenced application, kindly amend the application as follows: